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024 9729

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re PATENT APPLICATION OF

Gangadhar D. Bhat

Group Art Unit: Not Assigned

Appln. No.: Not Assigned

Examiner: Not Assigned

Filed: January 30, 2002



Title: INTERMEDIATE DRIVER HAVING A FAIL-OVER FUNCTION FOR A VIRTUAL NETWORK INTERFACE CARD IN A SYSTEM UTILIZING INFINIBAND ARCHITECTURE

Date: January 30, 2002

\* \* \* \* \*

**INFORMATION DISCLOSURE STATEMENT**

The attached Information Disclosure Statement is being filed concurrently with the application for patent.

This IDS is intended to be in full compliance with the rules, but should the Examiner find any part of its required content to have been omitted, prompt notice to that effect is earnestly solicited, along with additional time under Rule 97(f), to enable Applicant to comply fully.

Consideration of the foregoing and enclosures plus the return of a copy of the enclosed Form PTO-1449 with the Examiner's initials in the left column per MPEP 609 are earnestly solicited along with an early action on the merits.

Respectfully submitted,

Pillsbury Winthrop LLP

A handwritten signature in black ink, appearing to read "Roger R. Wise", written over a horizontal line.

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To: U.S. Department of Commerce  
(PW FORM PAT-1449)  
Patent and Trademark Office

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Applicant: Gangadhar D. Bhat

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**INFORMATION DISCLOSURE STATEMENT  
BY APPLICANT**

Date: January 30, 2002

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**U.S. PATENT DOCUMENTS**

Examiner's Initials*		Document Number	Date MM/YYYY	Name (Family Name of First Inventor)	Class	Sub Class	Filing Date (if appropriate)
	AR						
	BR						
	CR						
	DR						
	ER						
	FR						
	GR						
	HR						
	IR						
	JR						
	KR						
	LR						
	MR						
	NR						

**FOREIGN PATENT DOCUMENTS**

							Abstract		Readily Available	
		Document Number	Date MM/YYYY	Country	Inventor Name		Enclosed	No	Enclose	No
	OR									
	PR									
	QR									
	RR									
	SR									
	TR									
	UR									
	VR									
	WR									
	XR									

**OTHER (Including in this order Author, Title, Periodical Name, Date, Pertinent Pages, etc.)**

	YR	<a href="http://developer.intel.com/design/network/products/npfamily/ixp1200.htm">http://developer.intel.com/design/network/products/npfamily/ixp1200.htm</a> "Intel IXP1200 Network Processor Family." pp 1 - 3.			
	ZR				
	AAR				
	BBR				
	CCR				
	DDR				

Examiner

Date Considered: